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PATENT
Docket No. 15436.247.35.1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Certificate

DiJaili et al.

AUG 18 2008

of Correction

Serial No.: 10/020,527

Art Unit
3663

Filed: Dec. 14, 2001

For: OPTICAL CROSSBAR USING LASING
SEMICONDUCTOR OPTICAL AMPLIFIERS

Examiner: Mark Hellner

Patent No.: 7,046,434 B1

Issue Date: May 16, 2006

REQUEST FOR A CERTIFICATE OF CORRECTION
UNDER 37 C.F.R. § 1.323

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. § 1.323 which implement 35 U.S.C. § 255, the Commissioner is respectfully requested to issue a Certificate of Correction to correct mistakes in the above-identified patent. The enclosed Patent Office Form 1050 reflects the desired corrections. Also enclosed please find a check in the amount of \$100.00 to cover the costs for issuance of this certificate.

08/14/2008 HAL133 00000011.7046434
81 FC:1811 RECEIVED-USPTO
100.00 OP
AUG 18 2008 Patent Application

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 23-3178. Duplicate copies of this letter are enclosed.

Dated this 6 day of August, 2008.

Respectfully submitted,



R. Burns Israelsen
Attorney for Patentee
Registration No. 42,685
Customer No. 022913

RBI:jsb
Docket No. 15436.247.35.1
Certificate of Correction Documents (with check)

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 12 day of August, 2008.



Transmitted: REQUEST FOR A CERTIFICATE OF CORRECTION UNDER 37 C.F.R. § 1.323,
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159325

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AUG 18 2008



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15932S

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

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Page 1 of 6

APPLICATION NO. : 10/020,527

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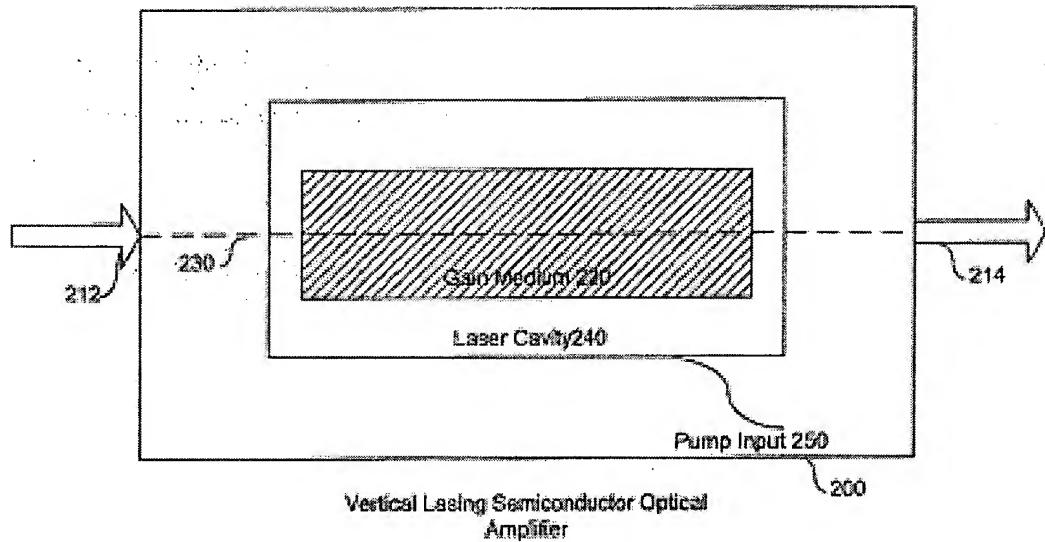


Figure 2

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R. Burns Israelsen, Esq., WORKMAN NYDEGGER
1000 Eagle Gate Tower
60 East South Temple
Salt Lake City, Utah 84111

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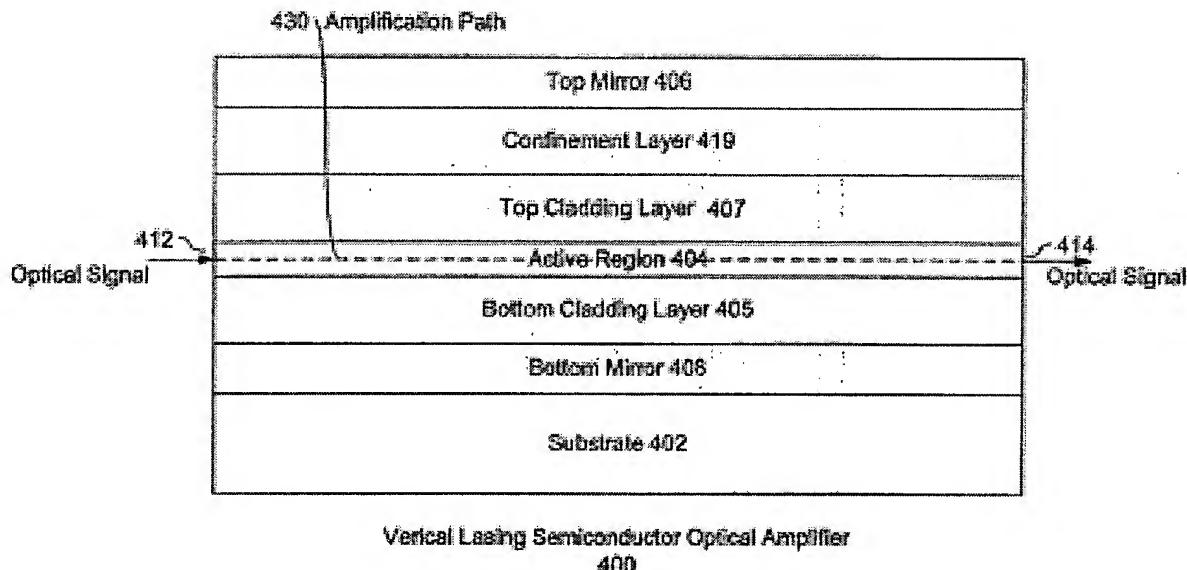


Figure 4C

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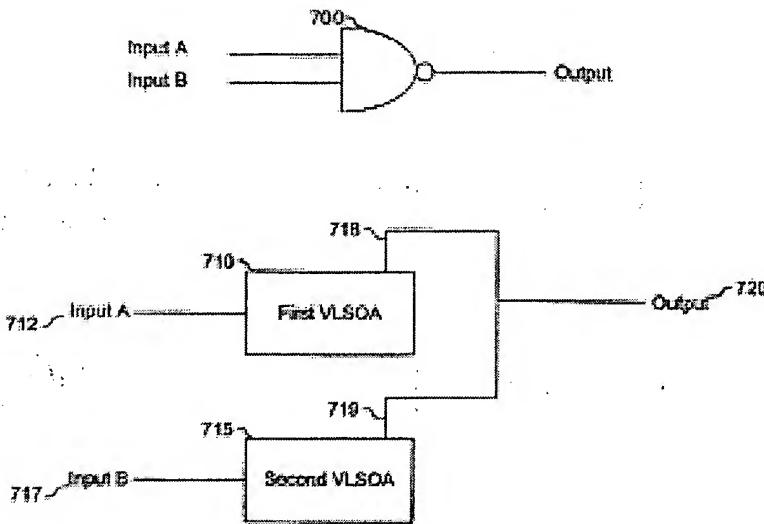
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Optical NAND Gate 700

Figure 7A

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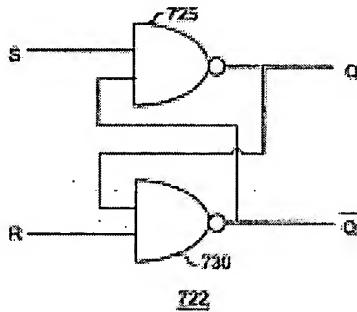
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S	R	Q	\bar{Q}	
0	0	1	1	Not Used
0	1	1	0	
1	0	0	1	
1	1	0	1	Memory State

Optical Flip-Flop

Figure 7B

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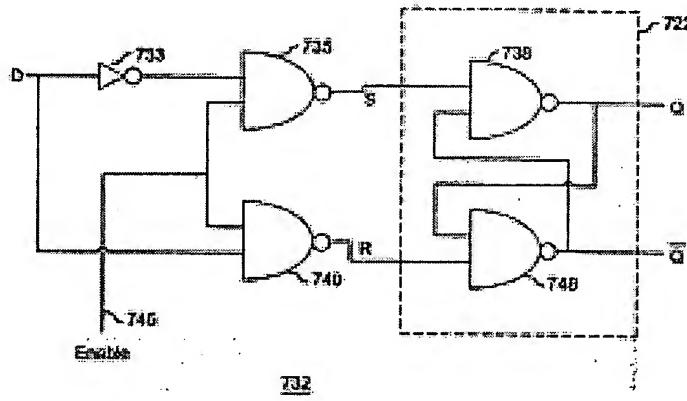
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D	Enable	Q	Q
0	0	Memory State	
1	0	Memory State	
0	1	1	0
1	1	0	1

Optoisolator Buffer Element
Figure 7C

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Line 43, change "30 dB" to --60 dB--

Column 12

Line 17, change "510" to --110--

Column 15

Line 18, change "FIG. 17B" to --FIG. 7B--

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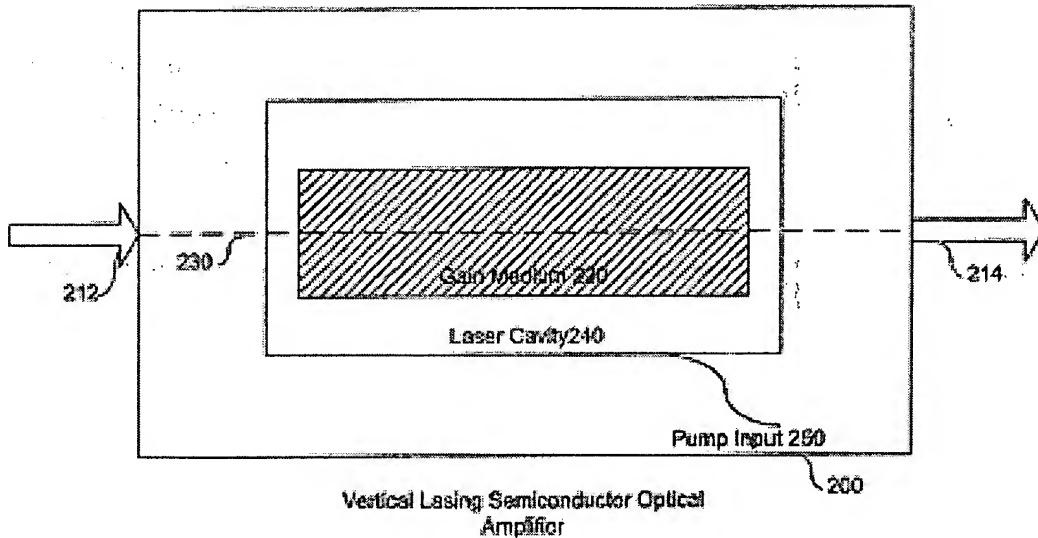


Figure 2

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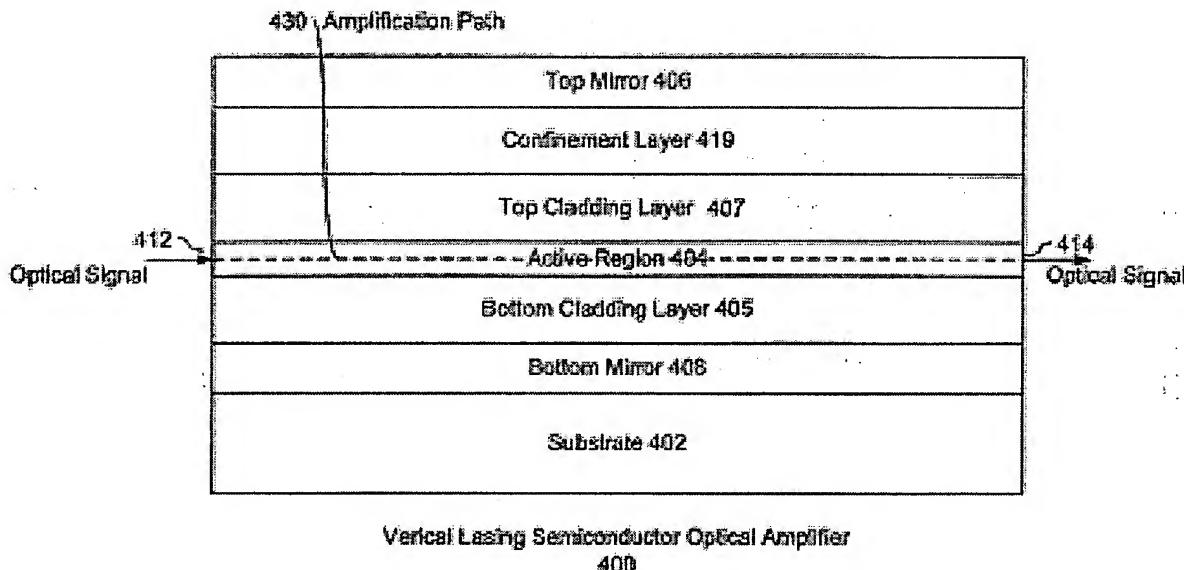


Figure 4C

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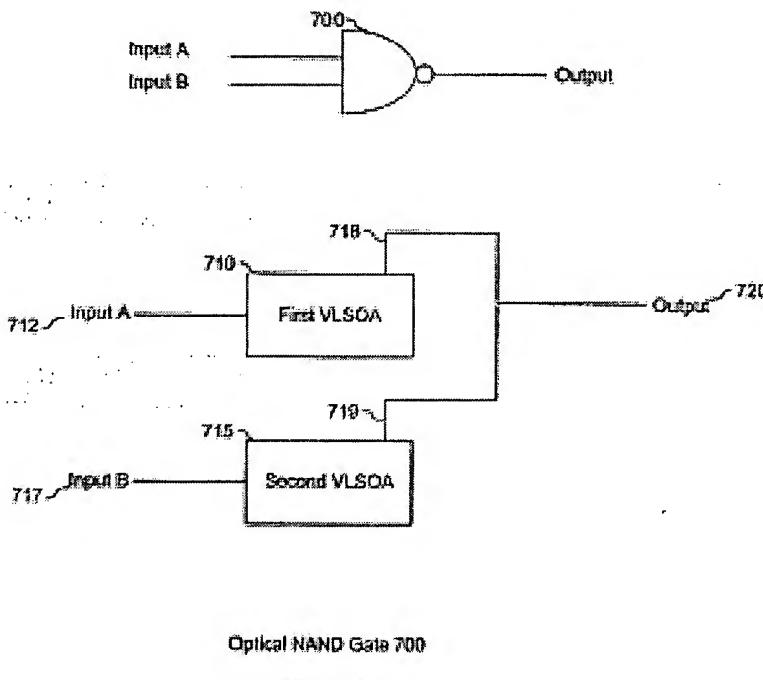
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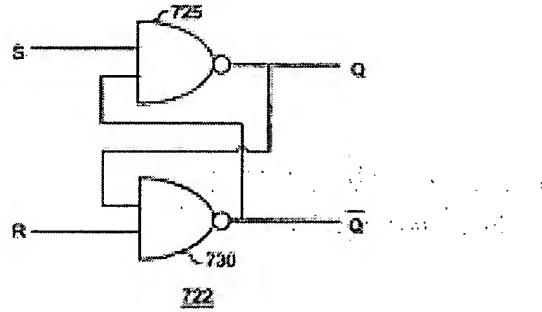
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S	R	Q	\bar{Q}	
0	0	1	1	Not Used
0	1	1	0	
1	0	0	1	
1	1	0	1	Memory State
		1	0	

Optimal Flip-Flop

Figure 7B

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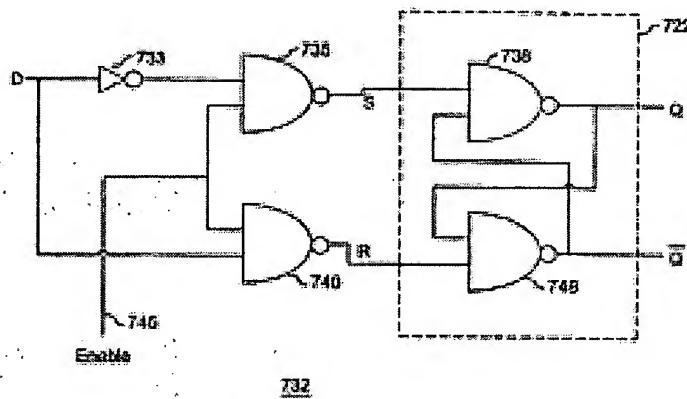
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D	Enable	Q	Q-bar
0	0	Memory State	
1	0	Memory State	
0	1	1	0
1	1	0	1

Optical Buffer Element
Figure 7C

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Patent and Trademark
AUG 18 2003

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